

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

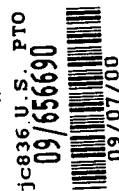
In re Patent Applicant of
Khandros et al.

Continuation Application of
Prior Application
Serial No. 09/488,268

Filed: Herewith

For: SEMICONDUCTOR CHIP ASSEMBLIES,
METHODS OF MAKING SAME AND
COMPONENTS FOR SAME

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: Group Art Unit:
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: Examiner:
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: Date: September 7, 2000



Assistant Commissioner for Patents
Box Patent Application
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INFORMATION DISCLOSURE STATEMENT

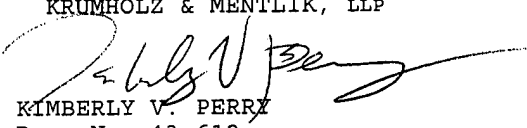
Sir:

It is respectfully requested that the references listed on the enclosed Form PTO-1449 be made of record and considered with respect to the above-referenced U.S. patent application. A copy of each reference is enclosed. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Respectfully submitted,

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